

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	18	702/120.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:20
L2	17	702/117.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:22
L3	78	"702"/\$.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:22
L4	0	700/53.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:28

L5	12	"700"/\$.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:29
L6	3	710/1.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:29
L7	0	710/20.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:29
L8	2	710/37.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:29
L9	3	710/52.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:30

L10	1	710/65.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:30
L11	89	"710"/\$.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:30
L12	64	714/30.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:32
L13	88	714/724.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:32
L14	67	714/726.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:33

L15	52	714/ 727.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/ 12/ 24 14:33
L16	21	714/ 729.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/ 12/ 24 14:34
L17	602	"714"/\$.ccls. and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/ 12/ 24 14:35
L18	376	17 and multiplexer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/ 12/ 24 14:39
L19	701	(test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3) and multiplexer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/ 12/ 24 14:40
L20	347	19 and (logic with gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/ 12/ 24 14:40

L21	20	waayers.in. and thomas	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:41
L22	1	21 and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3) and multiplexer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:41
L23	0	konninklijke adj philips adj electronics adj nv.as.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:42
L24	1	konninklijke adj philips adj electronics.as.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:42
L25	2769	nxp.as.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:43
L26	0	25 and (test\$3 with controller) and pin and register and (input and output) and (bit with pattern\$3) and (block\$3 or filter\$3) and multiplexer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/24 14:43

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